

Application No.: 10/640,349

Docket No.: JCLA11051

AMENDMENT

To the title:

Please amend the title of the invention appearing on page 1, lines 1-2, of the specification as follows:

CONTINUOUS GRAPHICS DISPLAY METHOD FOR MULTIPLE DISPLAY DEVICES
DURING THE PROCESSOR NON-RESPONDING PERIOD

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To the Specification:

Please amend the application as indicated hereafter.

Please amend paragraph [0009] beginning at line 18, page 5 as follows:

[0009] The increase of CPU work load often demands an increase in power consumption and results in a shorter battery life. The increased power consumption may cause a fatal problem to an application running on a mobile computer unit or a laptop computer. As a consequence, a variety of power saving techniques is introduced to the design of modern computers. ~~A couple of~~ Several power saving techniques are described as follows. When a CPU idles for a predetermined period of time, the CPU clock speed is reduced and the CPU power supply is turned off. Furthermore, instead of turning the [[CUP]] CPU on and off, a mechanism that detects the power consumption level of an application several times every second and self-adjusts the [[CUP]] CPU clock rate and power supply level to reduce power consumption is built into a computer. As a result, with the use of the power saving techniques, a battery can last longer and the capacity of a battery may be reduced to achieve the same performance.

Please amend paragraph [0039] beginning at line 8, page 17 as follows.

[0039] In Fig. 9, a flow chart diagram illustrates the mechanism of the third preferred embodiment of the present invention that properly extends display device blank periods to allow a power saving process to take place within the concurrent blank periods. First, providing sufficient system bandwidth, N display devices are connected to a computer system in step 901.

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The mechanism acquires the length of a power saving process period (PSPP) in step 902. Then, the mechanism waits to receive a power saving process signal that sends from the CPU and indicates a request for executing the power saving process by the CPU (step 903). Upon receiving the power saving process signal, the mechanism detects an upcoming blank period for each display device in step 904. Three display device blank periods are depicted in Fig. 10. The mechanism marks the occurrence of the last occurring blank period of the display devices as a reference point, in step 905, to allow the power saving process to take place. Just for illustrating purpose, the blank period of display device 2 is the last occur occurring blank period which is used as the reference blank period. Then, in step 906, the mechanism extends the rest of the blank periods of the other display devices until all the display devices have an overlapping blank period that can hold the system power saving process period, and executes the power saving process within the overlapping blank period in step 907. After the power saving process is finished, the mechanism goes back to the state of waiting to receive receive the next power saving process signal in step 903.